

<b>Notice of References Cited</b>	Application/Control No. 09/724,652	Applicant(s)/Patent Under Reexamination PANG ET AL.	
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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**FOREIGN PATENT DOCUMENTS**

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	Q					
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	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Evaluating the effects of SEUs affecting the configuration memory of an SRAM-based FPGA, Bellato, M. et al. Design, Automation and Test in Europe Conference and Exhibition, 2004. Proceedings , Volume: 1 , 16-20 Feb. 2004 Pages:584 - 589 Vol.1
	V	Configurable memory organisation for communication applications Soininen, J.-P.; Pelkonen, A.; Roivainen, J.; Digital System Design, 2002. Proceedings. Euromicro Symposium on , 4-6 Sept. 2002
	W	An SRAM-programmable field-configurable memory Ngai, T.; Rose, J.; Wilton, S.J.E.; Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995 , 1-4 May 1995
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.